

27 16

27 32

25 32

out bit (2) → high always (pull-up)
 out bit (14) → aan in bit (7) + pen 18 = /CE
 bit IN (11) → (6) → pen 20 = /OE

out bit (2) → low always
 out bit (14) → high always
 bit in (11) → t5 out (12)
 (5) V_{PP}^{IOE} → (6) → pen 20
 bit IN (7) → (10) → pen 18

out bit (2) → in bit (7) + pen 20 = /CE
 out bit (14) → low always
 IN bit (11) → X
 (5) V_{PP}^{t5} → (4) → (8) → pen 21

(5) V_{PP}^{t5} → (4) → pen 21

(6) ↑ coole voet pin

original zero standard selecties

ZIF28-1

Vpp setting

open 25V // gnd 21V
(was niet in gebruik)

Output bit controle
(via D5)

massa / gnd

ZIF24-
was pen 21 // ZIF28-
nu 23
All of Vpp

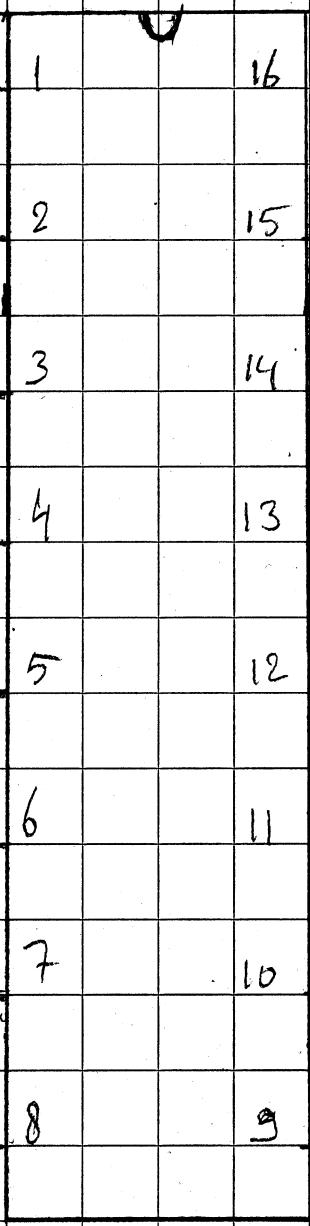
Vpp IN via +5 / +Vpp // IOE
switch

IOE / OE of Vpp out
was ZIF24-20, nu ZIF20-22

A1 Input bit t.b.v. IOE IN

2764 +
27128 extra sw input to gnd
voor 405x switch IC

Read / Program
(zakraan code voet 4)
Rd = 0 Pgm = 1



naar pen 1-28 pens voet = Vpp

naar ZIF28-27
pen 27-28 pens voet = PGM
(zakraan code voet 7)

output bit controle
via D6

+5V

Read pin out naar SW,
normal van 13 = +5 of OE

A2 Input bit IN t.b.v. IOE
2716 en 2732

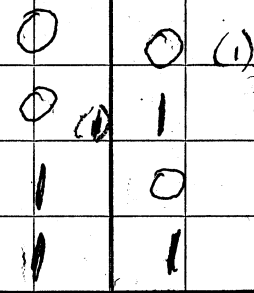
out IOE of A1
was 18 (24p)
nu 20 - 28 polig
ZIF28-20

A1 IN van
adres teller 4040
put

Pen 14 Pen 2

setting controle

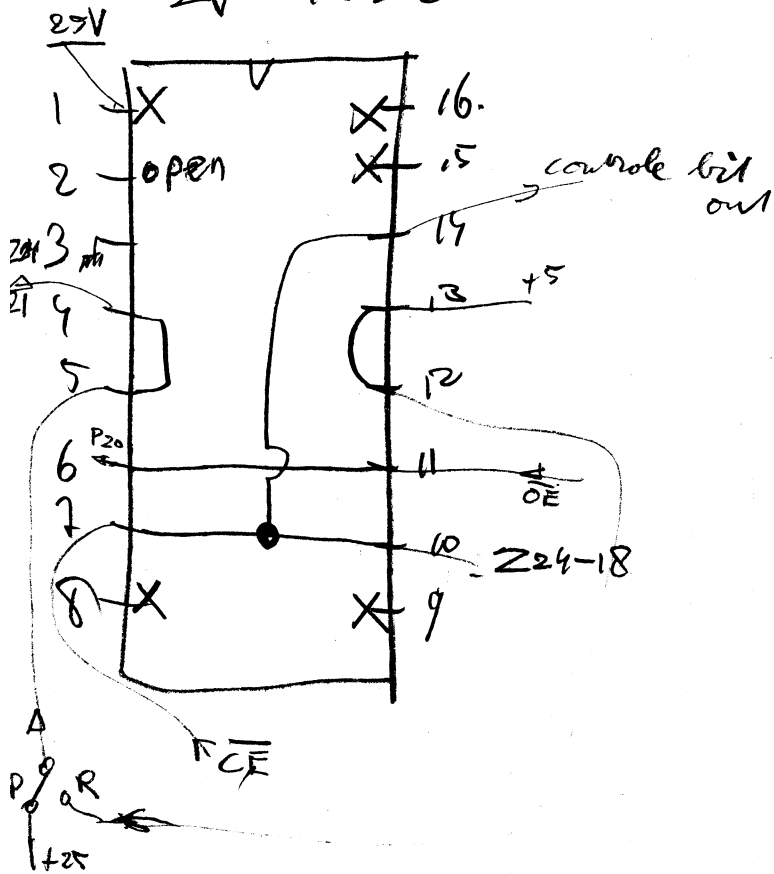
juiste jumpers



2532
2716
2732
WRONG

Aan gepaste / nieuwe definitie Zero code voet
voor piggy back board + 28pTEXTOL voet

2716/2516

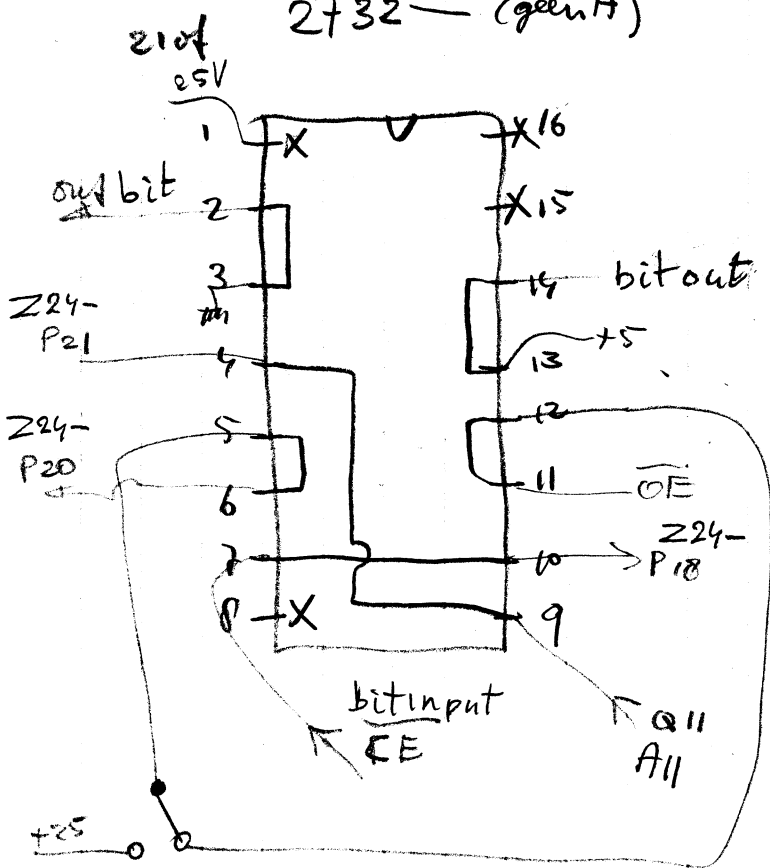


	2716	2516
P20	\overline{OE}	\overline{CS}
	\overline{CE}/PGM	PD/PGM
	$\Omega \uparrow$	$\Omega \uparrow$

2 "open" = pull-up = 1

2716/2516

2732 (green A)



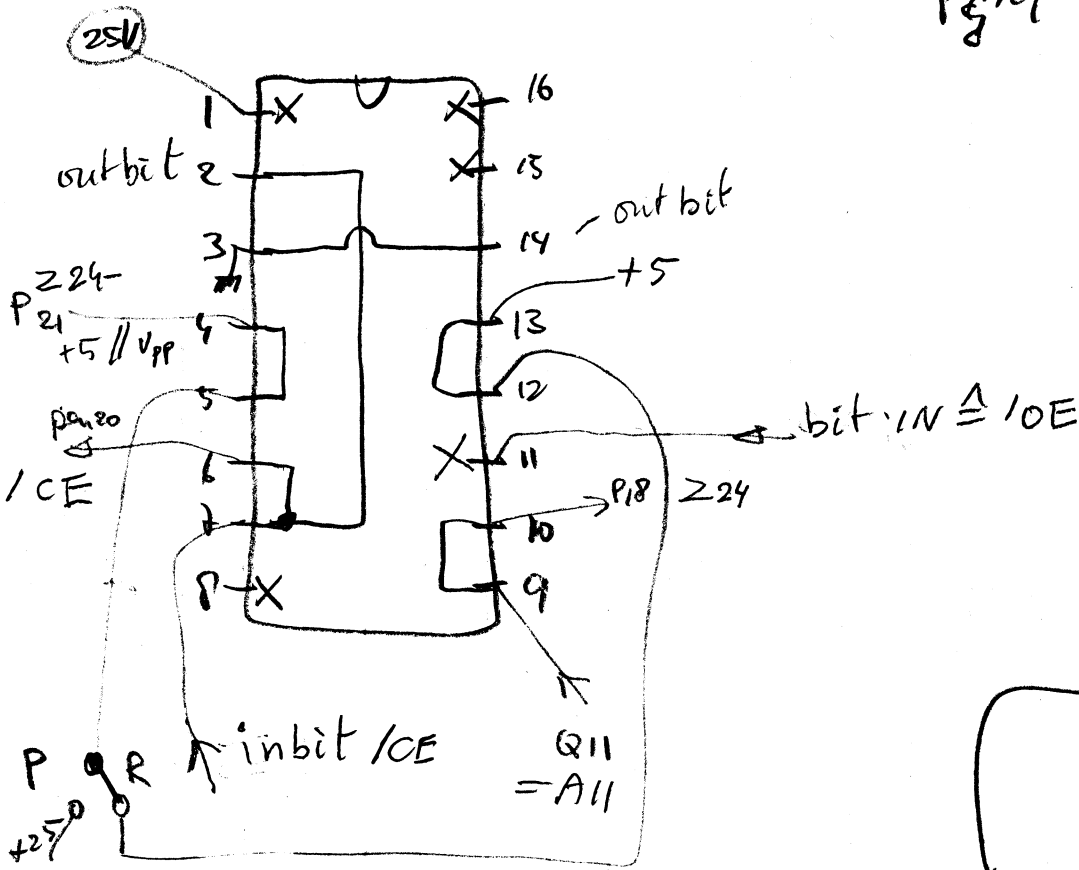
alleen
2732 A

1 en 3
dooromb.
 Δ 21V
1P/25

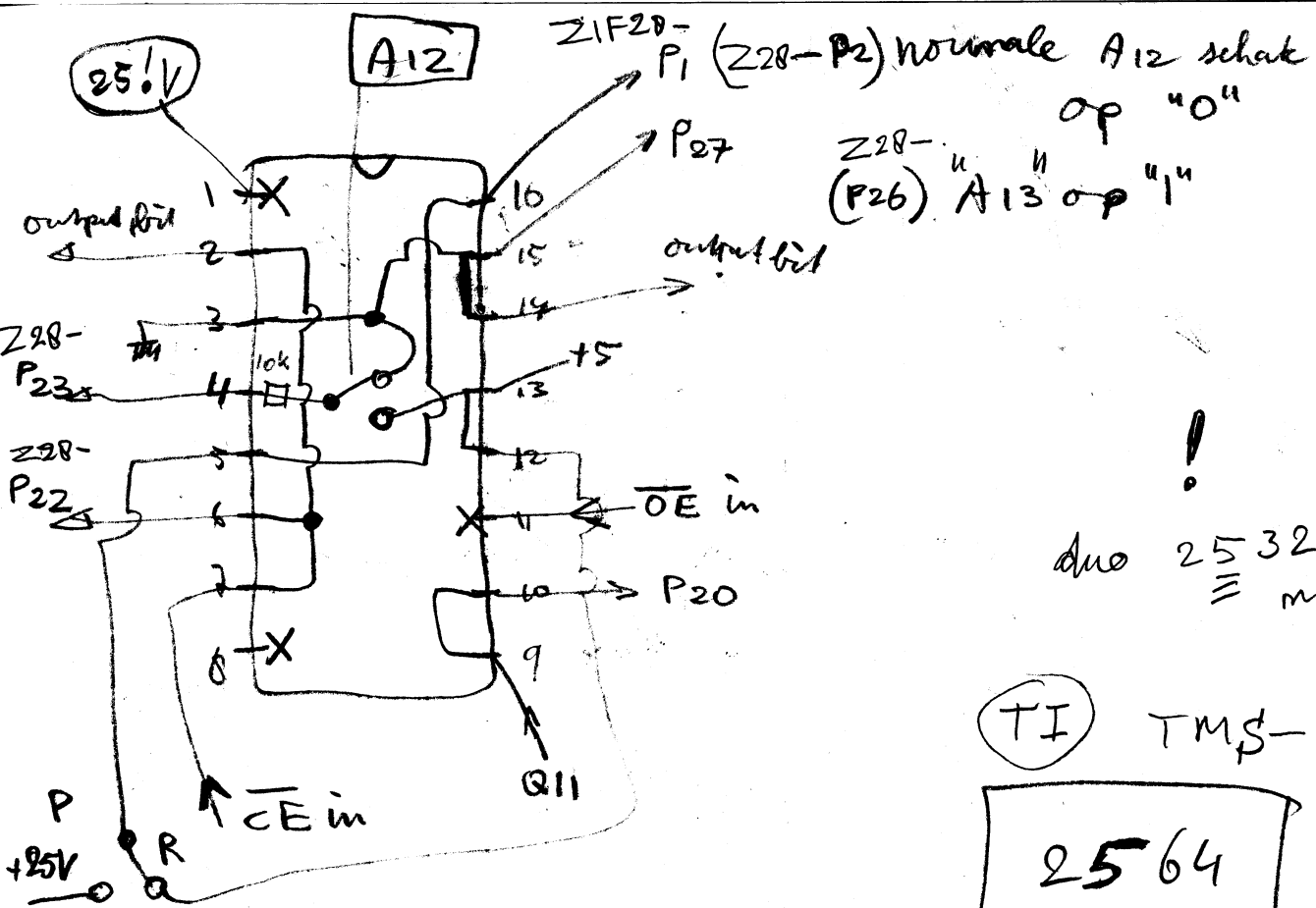
PGM $\Omega \downarrow$

2732
zonder A

2532



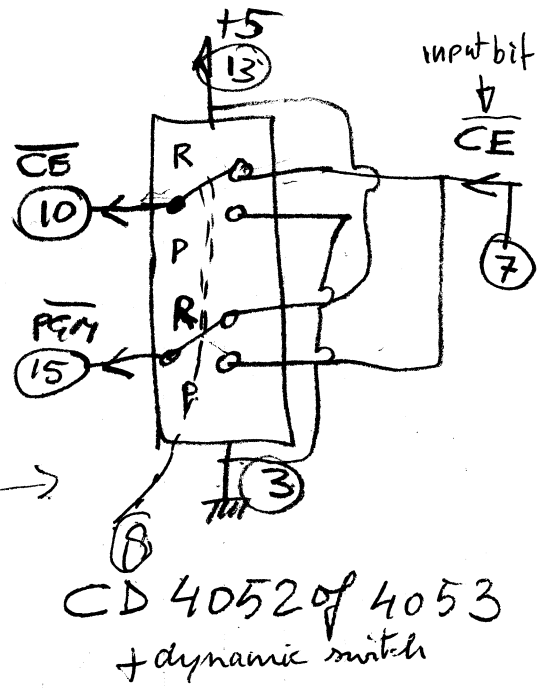
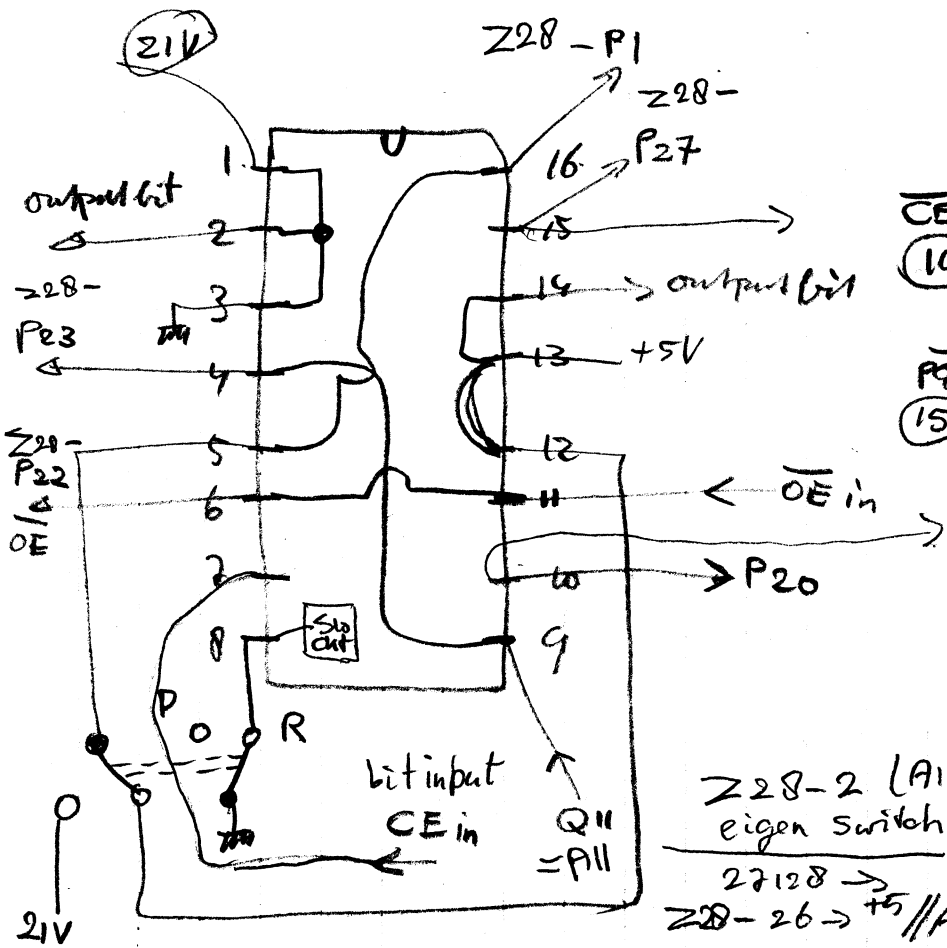
2532



!
duo 2532
≡ mode

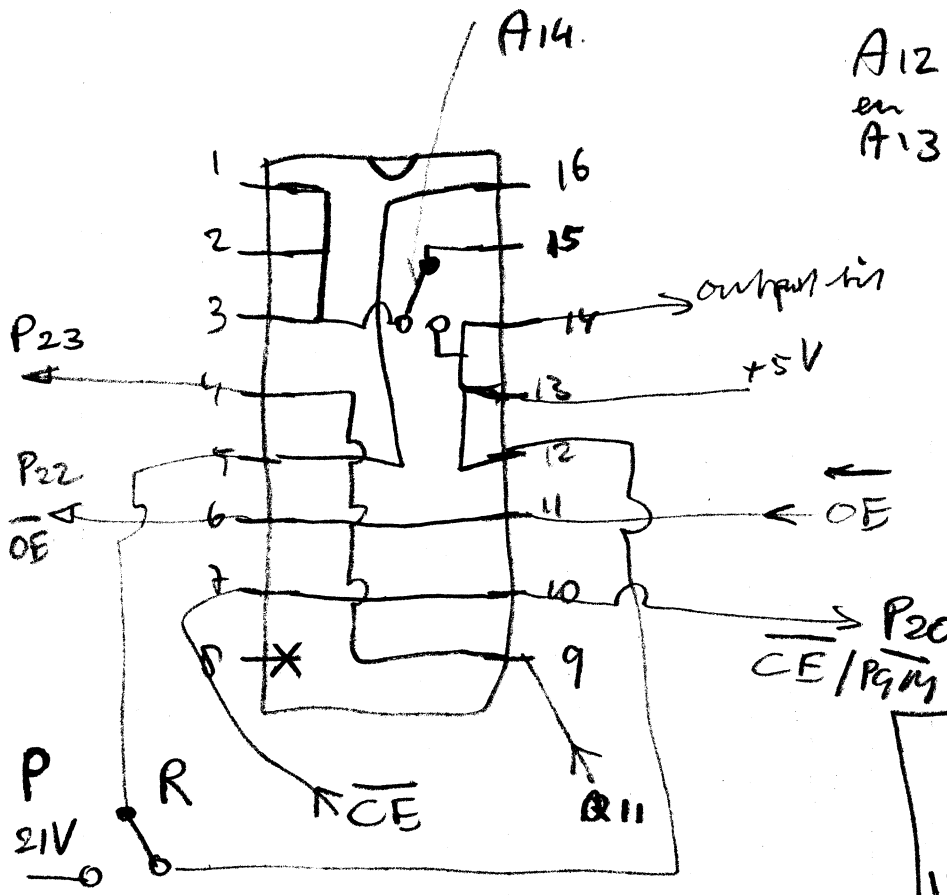
TI TMS-

2564



Z28-2 (A12)
 eigen switch
 27128 →
 228-26 → +5V // A13

CD 4052 of 4053
 + dynamic switch
 2764 en / 128

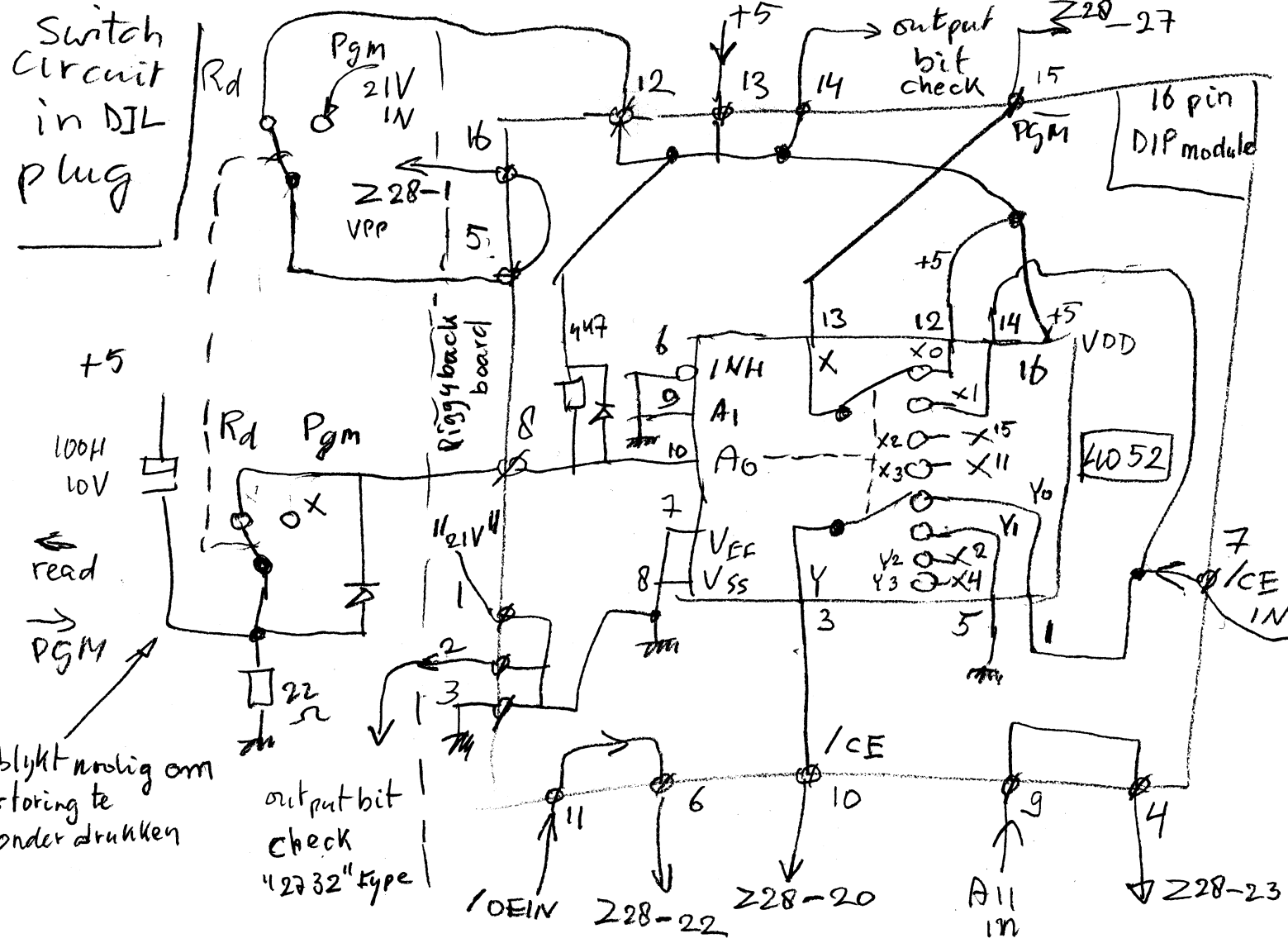


A12
 en
 A13 } normaal

Ⓢ met nodig
 t.b.v. dynamisch
 switch

Vpp meestal
 +12V
 R/O!
 27256

Switch circuit in DIL plug



read
PGM

blykt nrolig om storing te onder drukken

output bit check "2732" type

16 pin DIP module

4052

10E IN

228-22

228-20

All in

228-23

